

CPU Chip Having Registers Therein
for Reporting Maximum CPU Power and Temperature Ratings

5 FIELD OF THE INVENTION

 This invention relates generally to automatic thermal and power management in computer systems.

 BACKGROUND

10 In many computers, system firmware known as the basic input/output system ("BIOS") begins executing prior to transferring control of the computer to the operating system. During this initial execution, the BIOS typically performs a series of routines known as the power-on self test ("POST"). Even after POST has completed and the operating system has booted, the BIOS continues to provide basic
15 services as needed as long as the computer is powered on.

 Among the basic services provided by the BIOS in some computers is to control the speed of cooling fans disposed within the computer enclosure. Assuming that the BIOS knows the maximum temperature at which a central processing unit ("CPU") is rated by its manufacturer to operate, it can adjust fan speeds accordingly
20 in response to the actual temperature sensed in the CPU at a given point in time. This is done so that fans can be run at lower rates of speed when it is possible to do so, thus reducing power consumption and lowering noise levels.

 Currently, the BIOS is able to read registers within a CPU to determine the identity or model of the CPU. For example, existing registers might tell the BIOS that a CPU is an Intel Pentium IV. In order to make this information useful from a
25 thermal management perspective, however, the BIOS must keep cross-reference tables so that it can determine the maximum temperature at which a particular model

CPU is rated by its manufacturer to operate. When manufacturer specifications change for a given CPU model, or when new CPU models are introduced, the BIOS must be updated with new cross-reference tables.

5 SUMMARY OF THE INVENTION

In one aspect, the invention includes a CPU chip having a register or registers therein for reporting the maximum temperature at which the CPU is rated by its manufacturer to operate, or the maximum power the CPU is rated to consume, or both. The values may be generated by testing individual dies, and therefore may be
10 specific to the individual CPU. Or the values may be generic to a manufacturing lot or to a CPU model or version, and therefore may be generic to a group of CPU chips. In either case, the values may be placed in the register or registers when the CPU chip is manufactured or tested. The register or registers may be read-only registers to prevent corruption of the values after shipment. The register or registers may also be
15 nonvolatile read/write registers.

A computer system configured in accordance with the invention provides a number of advantages. For example, the BIOS in such a system does not have to be updated as frequently as in prior art computer systems. In addition, the BIOS may improve acoustics by tailoring fan speeds to the individual characteristics of the
20 installed CPU.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram illustrating a CPU chip having MaxCPUTemp and MaxCPUPower registers therein according to a preferred embodiment of the
25 invention.

Fig. 2 is a flow diagram illustrating a method for verifying compatibility of components in a computer system.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

5 Fig. 1 is a block diagram illustrating a CPU chip 100 according to a preferred embodiment of the invention. CPU chip 100 contains a MaxCPUTemp register 102 and a MaxCPUPower register 104. MaxCPUTemp register 102 contains a value that indicates the maximum temperature at which CPU chip 100 is rated to operate. MaxCPUPower register 104 contains a value that indicates the maximum power CPU
10 chip 100 is rated to consume during operation. In one embodiment, registers 102 and 104 may be two separate registers. In an alternative embodiment, they may be one combined register that holds both the MaxCPUTemp value and the MaxCPUPower value. Further references herein to either of the two registers are intended to include either possibility. Preferably, the registers 102 and 104 are capable of being read
15 with an appropriate CPU instruction.

The MaxCPUTemp and MaxCPUPower values may be placed in registers 102 and 104 when CPU chip 100 is manufactured or tested. In one scenario, each CPU chip manufactured may be individually tested. In this scenario, the MaxCPUTemp and MaxCPUPower values placed in registers 102 and 104 would
20 reflect values that are specific to that individual CPU chip. In a second scenario, tests may be performed on a manufactured lot of CPU chips (such as, for example, all CPU chips manufactured from a particular wafer) to determine temperature and power characteristics applicable to the lot. In a third scenario, the values written into registers 102 and 104 may simply reflect the specifications for all CPU chips having
25 a particular model or version number (for example, all Pentium IV chips having a certain step number, or all such chips rated to operate at a particular clock frequency).

In either of the first three scenarios, registers 102 and 104 may be configured as read-only registers so that the values contained therein would not be corrupted or lost after the CPU chip has been shipped. In a fourth scenario, registers 102 and 104 could be configured as nonvolatile read/write registers so that values could be placed therein based on testing that occurs after the CPU chip has been shipped.

Fig. 2 illustrates a method of using CPU chip 100 to verify compatibility of components in a computer system. In one embodiment, the method may be implemented in the BIOS. In alternative embodiments, the method may be implemented in other firmware, or in software or hardware. Although the description given herein will refer periodically to the BIOS as the acting agent, the description is intended to apply to either possibility.

One suitable time to execute method 202 is during POST, after the computer has been powered on in step 202 and before booting the operating system in step 224. The method may, however, be executed at other times as necessary or desirable. In steps 204 and 206, the BIOS determines the identity of the motherboard and the chassis of the host computer. This may be done using any available technique. One technique for identifying the motherboard is to read a register on the motherboard containing identifying information. A technique for identifying the chassis is to read a hardwired value coded onto the pins of any of the chassis connectors.

In step 208, the BIOS reads register 104 to determine the value of MaxCPUPower. In step 210, the BIOS determines a MaxHostPower value. Preferably, this value should indicate the maximum power that the host computer is capable of supplying to the CPU. Determining MaxHostPower may be done using any available technique. One technique is to use information gleaned from steps 204 and 206. For example, the identity of the motherboard may imply the existence of certain voltage regulation equipment. Or, the step of reading the identity of the

motherboard may include reading voltage regulation characteristics from registers on the motherboard. Likewise, the identity of the chassis may imply the existence of certain power supply and cooling fan equipment. Or, the step of reading the identity of the chassis may include reading power supply and cooling characteristics directly from registers or hardwired locations. Once this information has been collected by suitable means, a lookup table may be consulted or an expression may be evaluated to determine the value of MaxHostPower.

In step 212, the BIOS compares MaxCPUPower with MaxHostPower. If MaxCPUPower exceeds MaxHostPower, then an error handling routine is invoked in step 214. A suitable error handler may, for example, cause an error message to be displayed on the host computer to alert the user. The error handler may also cause the computer to power down automatically. Or the error handler may display the error message, wait for a suitable amount of time (ten seconds, for example), and then power down the computer. Any of a variety of other error handlers may also be employed.

If MaxCPUPower does not exceed MaxHostPower, then execution continues with step 216. In step 216, the BIOS reads register 102 to determine the value of MaxCPUTemp. (As was noted above, the values of MaxCPUTemp and MaxCPUPower may come from different registers of CPU chip 100, or they may come from the same register.) In step 218, the BIOS determines a MinHostTemp value. Preferably, this value should indicate the minimum CPU temperature the host computer is capable of maintaining. Determining MinHostTemp may be done using any available technique. One technique is to use information gleaned from steps 204, 206 and 208. For example, once information has been collected about the cooling characteristics of the chassis/motherboard combination, a lookup table may be

consulted or an expression may be evaluated to determine the value of MinHostTemp given the value of MaxCPUPower.

5 In step 220, the BIOS compares MaxCPUTemp with MinHostTemp. If MinHostTemp exceeds MaxCPUTemp, then an error handling routine is invoked in step 222. The error handler invoked in step 222 may be the same as or different than the error handler invoked in step 214, as appropriate. For example, the two error handlers may present different error messages on the display of the host computer. But if MinHostTemp does not exceed MaxCPUTemp, then execution continues normally at step 224.

10 When the computer is operating, the BIOS may use the information obtained during the above-described steps to tailor the speed of cooling fans responsive to the characteristics of CPU chip 100, thus possibly improving power consumption and noise levels in the host computer.

15 While the invention has been described in detail in relation to preferred embodiments thereof, the described embodiments have been presented by way of example and not by way of limitation. It will be understood by those skilled in the art that various changes may be made in the form and details of the described embodiments, resulting in equivalent embodiments that remain within the scope of the appended claims.